

FACSIMILE COVER SHEET

DATE: October 15, 2003

TO: Examiner David A. Zarneke

FAX NO.: 703-308-7722

USPTO GPAU 2827

FROM: J. Gustav Larson

RE U.S. App. No.: 10/035,518, filed 11/09/2001

Applicant(s): Vincent K. Chan et al.

Atty Dkt No.: 1376.0100750

Title: DIRECT INTERCONNECT MULTI-CHIP MODULE, METHOD FOR

MAKING THE SAME AND ELECTRONIC PACKAGE

COMPRISING SAME

NO. OF PAGES (including Cover Sheet): 10

MESSAGE:

Attached please find:

Transmittal Form (1 pg)

Amendment and Response to Examiner's Restriction Requirement (7 pgs)

Petition for Extension of Time (1 pgs)

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CERTIFICATE OF TRANSMISSION/MAILING

10-15-03

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Vincent K. Chan et al.

Title:

DIRECT INTERCONNECT MULTI-CHIP MODULE, METHOD FOR

MAKING THE SAME AND ELECTRONIC PACKAGE COMPRISING

SAME

App. No.:

10/035,518

Filed:

11/09/2001

Examiner:

David A. Zarneke

Group Art Unit: 2827

Atty. Dkt. No.: 1376.0100750

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO **EXAMINER'S RESTRICTION REQUIREMENT**

Dear Sir:

In response to the Examiner's Office Action Summary mailed August 15, 2003, please amend the above-identified application as follows:

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